

Absolute Multiturn Motor Feedback Encoder Standard Performance for Servo Motor and Drive

- Low-profile absolute multiturn encoder for standard and functional safety motion control
- Very compact absolute multiturn encoder with mounting depth less than 20 mm
- High Resolution 16 bit up to 20 bit Singleturn + 12 bit Multiturn
- Digital BiSS-C or SSI interface
- Analogue output for Functional Safety monitoring
- Encoder for Functional Safety applications (SIL2 PLd, SIL3 PLe, category 3)
- Dual-Sensor design prepared for functionally safe applications (optional)
- Wide operating temperature range up to +120°C
- Speed up to 12000 rpm without self-heating due to bearing less design
- Electronic Data Sheet (EDS) Motor and drive data can be stored inside encoder
- Encoder temperature sensor (BiSS-C)



TECHNICAL DATA mechanical

ACURO IBISS







drive	Certification in progress
Housing diameter	38 mm
Shaft	8 mm shaft, cone 1:3, M4 screw or
Mounting depth	20 mm, with plug 22 mm
Mounting flange	Direct flange/shaft mount

Mounting depth	20 mm, with plug 22 mm
Mounting flange	Direct flange/shaft mount
Protection class shaft input	- IP20
Protection class housing	- IP20
Axial endplay of mating shaft	±0.5 mm max. @ nominal position
Max. speed	12 000 rpm
Starting torque typ.	n/a
Moment of inertia	0.2 kgmm ² (rotor with screw M4x30)
Vibration resistance (DIN EN 60068-2-6)	300 m/s² (10 2000 Hz) 100 m/s² (10 2000 Hz) min.
Max. angular acceleration	100000 rad/sec ²
Shock resistance (DIN EN 60068-2-27)	3000 m/s² (6 ms)
Material shaft	Stainless steel
Material housing / cover	Aluminum / plastic
Weight	50g
Connection (with strain relief)	ECU Interface - PCB connector axial
Operating temperature ¹	-40°C +120°C

¹ see measuring point M1, dimensional drawings page 3.

Storage temperature

Specifications subject to change without notice

-30°C ... +80°C

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General design

Functional Safety Design

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TECHNICAL DATA electrical

5 V DC +10%/-5%, GND Supply voltage Power consumption 0.6 W Resolution singleturn 16 Bit - standard performance *other resolution on request 18 Bit - advanced performance 20 Bit - high performance 12 Bit multiturn - mechanical gear box Resolution multiturn Resolution incremental signals Standard: 256 periods sine+cosine (SinCos) Safety: 512 periods sine+cosine (SinCos) Inc. AB: 1-4096 ppr (TTL) Electrical Interface RS422 - BiSS-C or SSI (Clock, Data) 1Vpp - sine+cosine (SinCos); TTL - AB incremental Data Protocol BiSS-C / SSI gray Electronic Data Sheet (EDS) 512 bytes of storage for encoder data **OEM Memory** Additional storage for motor-, drive data (BiSS-C) Absolute accuracy (typ.) $\pm 0.033^{\circ} / \pm 2' / \pm 120''$ (after autocalibration²) $\pm 0.0055^{\circ} / \pm 0.33' / \pm 20''$ (after autocalibration²) Repeatability (typ.) ² follow the instructions in the manual for automatic adjustment features on command (see BiSS-C encoder adjustment)

as per EN IEC 61010-1, protection class III,

contamination level 2, overvoltage class II

SIL2, SIL3 according to EN IEC 61508, 62061, 61800-5-2/-3

TECHNICAL DATA safety (optional)

Tunicuonal durcty besign	PLd, PLe according to EN ISO 13849-1
Functional Safety Architecture	Encoder(SR) with two independent channels: ♦ First channel: incremental (SinCos) and ♦ Second channel: absolute (SSI or BiSS-C) output signals according to EN IEC 61800-5-3
Electrical Interface	1Vpp differential signals @50% of supply voltage <i>(SinCos)</i> RS422 - SSI or BiSS-C <i>(Clock, Data)</i>
Resolution for Safety Function	9 bit singleturn, based on 512 sine+cosine periods
PFH-value	20 FIT
MTTFd	100 years
DCavg	90%
The encoder can support the following Safety Drive Functions according IEC 61800-5-2	Safety function based on speed and acceleration: SS1 Safe Stop 1 ¹⁾ SS2 Safe Stop 2 ¹⁾ SOS Safe Operation Stop SDI Safe Direction SLS Safe Limited Speed SLI Safe Limited Increment SLA Safe Limited Acceleration SSR Safe Speed Range SAR Safe Acceleration Range ¹⁾ deceleration controlled (-d) or ramp monitored (-r)
Safety evaluation	see Implementation Guide and User Manual "D-582-031"
Cycle time (Frame Repetition)	>30 µsec
Propagation delay /latency	0.25 µsec
Signal stabilization	Signal stabilization by auto-gain with monitoring
Compensation of misalignment	Automated adjustment during installation

Adjustable zero position

TECHNICAL DATA features

 Specifications subject to change without notice.

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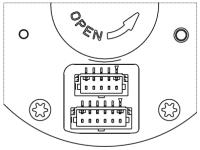
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Commutation



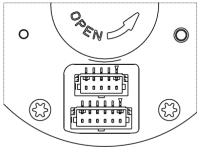
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ELECTRICAL CONNECTIONS



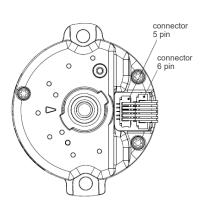
DIMENSIONAL DRAWINGS

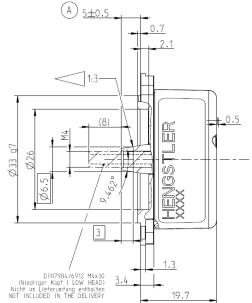
8 mm shaft, cone 1:3, M4 screw

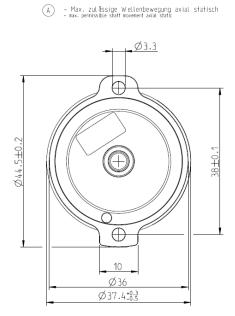


PCB-Coni	PCB-Connector ²				
Туре	Type T1M-06-GF-S-V-K-TR		Туре	T1M-0!	5-GF-S-V-K-TR
PIN	Signal SSI	/ BiSS-C (absolute)	PIN	Signal	1Vpp or TTL (incremental)
∇ 1	VDD		∇ 1	B+	(COS+)
2	GND		2	B-	(COS-)
3	DATA-	(RS422)	3	GND	
4	DATA+	(RS422)	4	A+	(SIN+)
5	CLOCK-	(RS422)	5	Α-	(SIN-)
6	CLOCK+	(RS422)			Pin assignment to interface signal

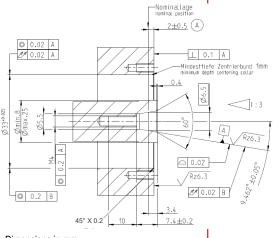
Insulation resistance according to EN IEC 60204-1 PELV/SELV

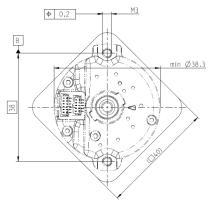


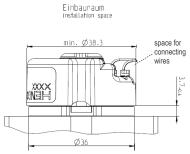




INSTALLATION REQUIREMENTS







Note: space for connecting wires are considered at the encoder. With a small bending radius (max. R2.7), the connection remain within the encoder outer contour.

Dimensions in mm Tolerances according DIN ISO 2768-m

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² SAMTEC T1M / ISS1 / S1SST



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ORDERING INFORMATION

Type ³	Resolution ⁴	Supply voltage	Flange, Protection, Shaft	Interface ⁵	Connection
AM34/	0014 14 Bit ST 0016 16 Bit ST	A 5 VDC	Z.OT 2-eared flange, IP20, 8 mm cone 1:3, M4	BE BiSS-C BU BiSS-C+SinCos 1Vpp	4 Absolute signals & power supply,
AM34S AM34E	0017 17 Bit ST 0018 18 Bit ST 0019 19 Bit ST 0020 20 Bit ST		Z.0F 2-eared flange, IP20, 6 mm hub shaft, M3	SG SSI gray SU SSI gray+SinCos 1Vpp	6 pole connector, axial Incremental signals, 5 pole connector, axial (available according to interface)
AM34D	1214 14 Bit ST + 12 Bit MT 1216 16 Bit ST + 12 Bit MT 1217 17 Bit ST + 12 Bit MT 1218 18 Bit ST + 12 Bit MT 1219 19 Bit ST + 12 Bit MT 1220 20 Bit ST + 12 Bit MT			BZ BiSS-C+TTL(1-4096ppr) SZ SSI+TTL(1-4096ppr) 4W ACURO link 4-wire 2L SCS open link 2-wire	9 Data & power supply, 4 pole, axial Motor temp. sensor, 2 pole, axial

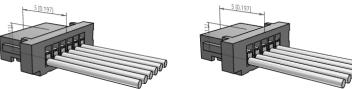
³ Type: AM34/: Standard

AM34S: Functional Safety (SIL2, PLd) - *certification in progress* AM34E: Functional Safety (SIL3, PLe) - *certification in progress* AM34D: Dual-Sensor (prepared for functionally safe applications)

[grey] Color coded version on request

ACCESSORIES Connection Cables / Plug

Connection cable / Plug	Part Nr.
Absolute signals cable, 20 cm, 6-pin	S1SST-06-28-GF-08.00-S
Incremental signals cable, 20 cm, 5-pin	S1SST-05-28-GF-08.00-S
Sticker to close the cap	E2531169
Plug to close the cap	E2545059
Plug with wire guides to close the cap	E2545052
F an .	



S1SST-06-28-GF-08.00-S

S1SST-05-28-GF-08.00-S



TECHNICAL MANUALS Impleme

	Ordering code
Implementation Guide and User Manual, English	D-582-031
Installation Instructions, English	D-582-034

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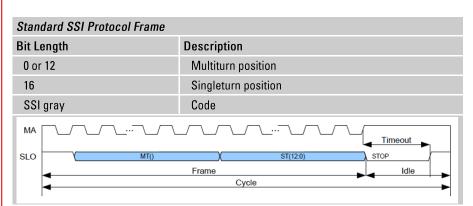
⁴Other resolution on request

⁵ Incremental signals (standard: 256 / safety: 512 periods sine+cosine); TTL - ppr on request [:PPRxxxx] ⇒ 0001 to 4096 ppr; Other interface on request

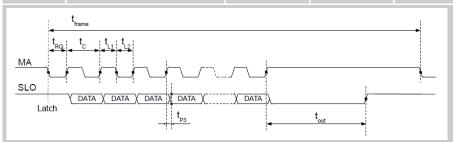


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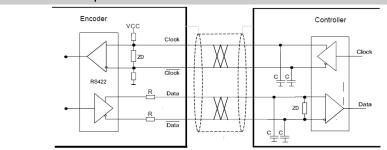
TECHNICAL DATA electrical interface SSI



SSI Timing	SSI Timing					
Symbol	Parameter	Min.	Max.	Unit		
t _{frame}	Permissible Frame Repetition	30	unlimited	μs		
1/t _C	Permissible Clock Frequency	0.1	1	MHz		
t _{L1}	Clock Signal hi Level Duration	250	t _{out}	ns		
t _{L2}	Clock Signal lo Level Duration	250	t _{out}	ns		
t _{RQ}	REQ Signal lo Level Duration	50		ns		
t _{out}	Slave Timeout	16	24	μs		
t _{P3}	Propagation Delay (SLO stable after MA lo hi)	60	250	ns		



Recommended input circuit Standard SSI or BiSS-C to 10 MHz clock rate



Dimensions: $R = 10 \Omega$, $Z0 = 120 \Omega$, C = 100 pF (if needed)

Cable Length ¹	SSI Clock Frequency	BiSS-C Clock Frequency*
< 25 m	< 1 MHz	< 10 MHz
< 50 m	< 400 kHz	< 10 MHz
< 100 m	< 300 kHz	< 10 MHz
< 200 m	< 200 kHz	*Details for BiSS-C see page 6 ff.
< 400 m	< 100 kHz	

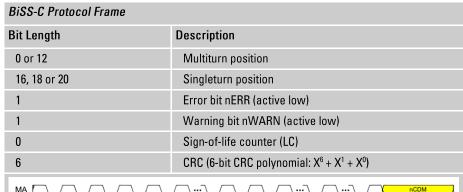
¹ Twisted pair wiring with ground shield on both sides recommended.

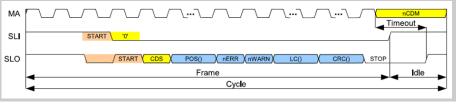
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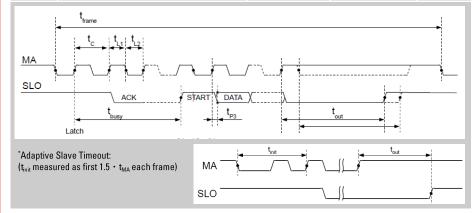
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TECHNICAL DATA electrical interface BiSS-C





BiSS-C I	liming			
Symbol	Parameter	Min.	Max.	Unit
t _{frame}	Permissible Frame Repetition	30	unlimited	μs
1/t _C	Permissible Clock Frequency	0.1	10	MHz
t _{L1}	Clock Signal hi Level Duration	25	t _{out}	ns
t _{L2}	Clock Signal lo Level Duration	25	t _{out}	ns
t _{busy}	Processing Time with Start Bit Delay		6 t _C	ns
t _{out}	Adaptive Slave Timeout $(t_{init} = 1.5 t_{MA})^*$	0.1	24	μs
t _{P3}	Propagation Delay: SLO stable after MA lo hi	60	250	ns



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Table of register assignment Name Size Address (Hex) Memo 0x00..0x3F Registerbank 64 Bytes 0x40**Bankselection** 0..8 Bits (1 Byte) 1) **EDS-Bank** 0x41 0..8 Bits (1 Byte) 1) 0x42..0x43 ProfileID 3) 16 Bits (2 Bytes) 0x44..0x47 3) Serialnumber 32 Bits (4 Byte) 0x4E..0x4F Temperature sensor 16 Bits (2 Bytes) 0x77 2) Command register 0..8 Bits (1 Byte) 0x78..0x7D 3) DeviceID 48 Bits (6 Bytes) 0x7E..0x7F ManufacturerID 16 Bits (2 Bytes) 3)

- 1) Bank selection e.g. for Encoder-Profil and BiSS-Identifier.
- 2) Supported commands see below.
- 3) The value is saved as a Big Endian, i.e. with the highest-value byte at the lowest-value address.

Bank selection

The bank selection register at address 0x40 selects a register bank consisting of up to 0x27 (39_{dec}) banks and displays these at the register addresses 0x00 to 0x3F.

- Bank 0x0E: Encoder-Profil and BiSS-Identifier; Bank 0x24: EDS BiSS Interface data;
- Bank 0x25: EDS Encoder data; Bank 0x26...0x27 OEM data

In order to send a **Command to the encoder**, the command (CMD) must be written into **register 0x77**. Example for command "Preset": Write the value 0x88 (MTST_PRESET_STORE) to the address 0x77. This sets the total position value (single and multiturn) to 0.

BiSS-C Interface - Commands

TECHNICAL DATA

BiSS-C Interface
- Register assignment

COMMANDS				
CMD (7	7:0) Addr. 0x77; b	it7:0 default: 0x00		
Code	Name	Description		
0x41	CONF_WRITE_ALL	Write current configuration of all banks to EEPROM. This includes the RPL information for all banks and the RPL information for the EEPROM. Valid CRC checksums are always calculated automatically beforehand for all banks.		
0x88	MINI PRESEL SILIRE	Identical to MTST_PRESET, additionally store the containing bank 0xBC to EEPROM.		
0x89	MIL PRESEL STURE	Identical to MT_PRESET, additionally store the containing bank 0xC to EEPROM.		
0xB0	ΔΙΙΙΙΙ ΔΙΙ.Ι ΔΙΝΔ	Automatic analog adjustment> Duration in seconds = 900/speed in rpm.		
0xB2	AUTU REAULT DIE	Automatic digital re-adjustment (in-field)> Duration in seconds = 1200/speed in rpm.		
0xB3	ΔΗΤΗ ΔΗ.Ι ΕΙ.Ι.	Automatic eccentricity adjustment> Duration in seconds = 15360/speed in rpm.		
0xFF	<nop_fail></nop_fail>	<return-code: failed="" last="" operation=""></return-code:>		
(*) The acti	ve bank is defined by BSEL; B	SEL must not be changed before the command finishes.		

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TECHNICAL DATA BiSS-C Identifier Encoder-Profile

Electronic Data Sheet (EDS) - EDS BiSS Interface data

Bank 0x0E	Bank 0x0E: Encoder-Profil and BiSS-Identifier			
Register	Value (HEX)	Category	Description	
0x41	0x24		First Bank of Electronic Data Sheet (EDS)	
0x42	0x62		BiSS Profile ID: Position Data	
0x43	0xXX		BiSS Profile ID: Data with Error and Warning (w/o CRC) e.g. 12bit MT + 24bit ST + ErrorBit + WarningBit -> 38 bits => 0x26	
0x44	0xXX	Profil		
0x45	0xXX		Serial number: 7 digits production identifier + 3 digits se-	
0x46	0xXX		quence number (max. 2 ³²)	
0x47	0xXX			
0x78	0xXX			
0x79	0xXX		Encoder part number (max. 2 ³²)	
0x7A	0xXX			
0x7B	0xXX	BiSS	Production date: Day (BCD format)	
0x7C	0xXX	Identifier	Production date: Month (BCD format)	
0x7D	0xXX		Production date: Year (BCD format)	
0x7E	0x48		Manufacturer ID: Hengstler -> "HE" (ASCII)	
0x7F	0x45		Wallulactuler ID. Hellystiel -> TE (ASCII)	

Bank 0x24: EDS BiSS Interface data Register Value Units Description 0x00 0x01 EDS version (continuous number) 0x01 0x02 Banks EDS length (bank count completely) 0x02 0x26 Bank address USER start (bank selection in address 0x40, 255= not available) 0x03 0x27 Bank address USER end (bank selection address 0x40) 0x04 0x64 Minimum permitted clock period on MA (TMA) 0x05 0x00 Minimum BiSS timeout (0= adaptive) (BiSS time = reg(value)*250ns) 0x06 0x00 Maximum BiSS timeout (0= adaptive) (BiSS time = reg(value)*250ns) 0x07 0x00 Minimum BiSS timeout_S (0= adaptive) (BiSS time = reg(value)*25ns) 0x080x00 Maximum BiSS timeout_S (0= adaptive) (BiSS time = reg(value)*25ns) 0x00 0x09Minimum sampling period adaptive timeout (0= adaptive timeout not available) 0x00 0x0AMaximum sampling period adaptive timeout (0= adaptive timeout not available) 0x00 0x0BMinimum cycle time (0= no limitation) 0x0C0x01Maximum processing time SCD 0x0D0x00TMA Additional processing time SCD in clocks 0x0E...0x0F 0x0000Maximum "power on delay" until control communication is available 0x10 0x01 Number of data channel in this device (number of words) 0x11 0x01 Area of validity for this EDS (number of slave addresses) 0x00 0x12 Memory location for this EDS (slave ID within this device) 0x13 0x00 Reserved 0x14 0x26 Bank address for content description data channel 1 (profile EDS) 0x15 0xXX bit Data length data channel 1 (MT+ST+EW+LC+CRC) 0x16 0x02 Data format data channel 1 0x17 0x43 CRC polynomial (8:1) for data channel 1 0x18 0x00 Bank address for content description data channel 2 (profile EDS) 0x19 0x00 Data length data channel 2 0x1A 0x00 Data format data channel 2 0x1B 0x00 CRC polynomial (8:1) for data channel 2 0x1C 0x00 Bank address for content description data channel 3 (profile EDS) 0x1D 0x00 Data length data channel 3 0x1E 0x00 Data format data channel 3 0x1F 0x00 CRC polynomial (8:1) for data channel 3 0x20...0x3E 0x00 Not used 0x3F

Check sum (addition of all bytes within this bank) Specifications subject to change without notice.

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0xXX



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TECHNICAL DATA

Electronic Data Sheet (EDS)

- EDS Encoder data

Register	Value	Units	Description	Meaning
0x00	0x01		BiSS Profile 3 Version	1
0x01	0x01	Bank	Length of this profile	1
0x020x03	0x61XX		Profile identification BP3 (content also available in address 0x42 and 0x43)	XX
0x04	0x01		Feedback bit 1 low active error status nE	ERR bit low active
0x05	0x02		Feedback bit 2 low active warning status nW	WRN bit low ac- tive
0x06	0x14	ms	Maximum "power on delay" until position data available	20
0x07	0x00		Reserved	0
0x08	0x00		Encoder type	Rotary encoder
0x09	0x00		Position value	Not defined
0x0A	0x00	bit	Data length MULTITURN	0
	0x0C			12
0x0B	0x01		Data format MULTITURN	Left aligned
0x0C	0x14	bit	Data length COARSE	20
0x0D	0x01		Data format COARSE	Left aligned
0x0E	0x00	bit	Data length FINE	0
0x0F	0x00		Data format FINE	Right aligned
0x100x13	0x00001000		Number of distinguishable revolutions/periods	4096
0x140x17	0x00000100	PPR	Number of signal periods per revolution/ length of signal period	256
0x180x1B	0x00000100		Resolution factor per signal period (LSB of the interpolation)	256
0x1C0x1F	0x00000043		CRC polynomial (32:1) 2)	67
0x200x23	0x00000000		CRC start value3)	0
0x240x25	0x0000	LSB/2	Absolute accuracy	0
0x260x27	0x0000	LSB/2	Repeat accuracy	0
0x280x29	0x0000	LSB/2	Angular speed/ speed depending accuracy	0
0x2A0x2B	0x0000	LSB/2	Hysteresis	0
0x2C0x2D	0x2710	rpm	Maximum revolution speed/ maximum speed	10000
0x2E0x2F	0x0000	1/min2	Maximum angular accelleration/ maximum acceleration	0
0x300x31	0x00E9	°C	Minimum operating temperature = reg(value)-273 _{dec}	-40
0x320x33	0x0184	°C	Maximum operating temperature = reg(value)-273 _{dec}	115
0x340x35	0x1194	mV	Minimum operating voltage	4500
0x360x37	0x157C	mV	Maximum operating voltage	5500
0x380x39	0x00C8	mΑ	Maximum current consumption	200
0x3A0x3E	0x00		Reserved	0
0x3F	0xXX		Checksum (sum of bytes in 0x00 0x3E)	XX

ENCODER ADJUSTMENT

- analog signals
- digital signals
- eccentricity

Note on BiSS-C:

For the commands for adjusting the analog signals (0xB0), the digital adjustment (0xB2) and the eccentricity adjustment (0xB3), the encoder must be driven at a speed that is as constant as possible (300 rpm ... 1000 rpm).

Depending on the speed, a time must be observed during these adjustment processes during which no communication with the encoder should take place.

-> Duration in seconds: 0xB0 = 900/speed in rpm; 0xB2 = 1200/speed in rpm; 0xB3 = 15360/speed in rpm.

After the adjustment, the configuration must be saved:

-> Command 0x41 save the configuration.

Documents for BiSS-C and the EDS can be found on the following website: https://biss-interface.com/c/downloads

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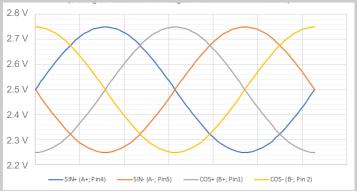
Absolute Multiturn Motor Feedback Encoder Standard Performance for Servo Motor and Drive

TECHNICAL DATA electrical interface Sin/Cos

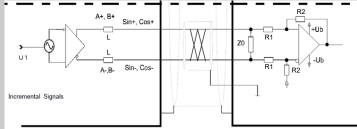
Analogue output signals SIN/COS			
Output signal	Description		
A+	Sine+ (SIN+)		
A-	Sine- (SIN-)		
GND	Signal grounds (analog/digital) internally connected		
B+	Cosine (COS+)		
B-	Cosine- (COS-)		

Characteri	istics analogue output signals SIN/Cl	os		
Symbol Parameter		Value	Tolerance	Unit
P _n	Sine/Cosine periods per turn (360°)	256		#
f() _{sin/cos}	Frequency Analog	60		kHz
Vout()dc	Output Signal DC Level (typ. 2.5V DC)	50	±3	%VDD
Vout()ac Output Signal AC Amplitude		250		mV
I()mx	I()mx Permissible Load Current			mA
C()mx	Permissible Capacitive Load	50		pF
α()e,mx	Maximum Permissible Acceleration α ()e = $d\omega$ /dt for sine/cosine	200		10 ⁶ rade/s ²
PH	Phase sine/cosine	90	±3	0

Signal output at clockwise rotation when looking at the encoder shaft



Recommended input circuit with incremental track 1 Vpp



Dimensions:

 $L=4.7\mu H$, $R1=10k\Omega$,R1 = R2, Z0 = 120 Ω

 $U1 = 2.5 V \pm 0.5 V$ (relating to supply voltage).

Twisted pair wiring with ground shield on both sides recommended.

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Characteristics incremental output signals AB

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TECHNICAL DATA

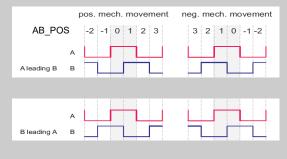
electrical interface AB

Characteristics output signals

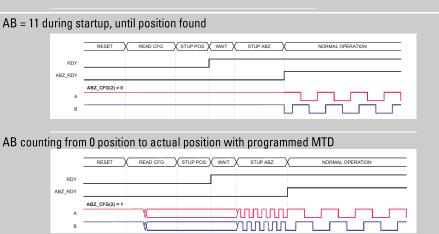
Incremental output signals AB (in preparation)				
Output signal	Description			
A+	A+ (TTL)			
A-	A- (TTL)			
GND	Signal grounds (analog/digital) internally connected			
B+	B+ (TTL)			
B-	B- (TTL)			

Characteristics incremental catput signals /12				
Symbol	Parameter	Value	Tolerance	Unit
P _n	AB periods per turn (360°)	1-4096		#
AB-HYS	AB hysteresis	0 / 0.044	(0 / 2 LSB)	°e
MTD	Minimum Transition Distance	100		nsec
f() _{AB}	Frequency incremental	960		kHz
Vout()hi	Output Signal Amplitude high	≥70		%VDD
Vout()lo	Output Signal Amplitude low	≤30		%VDD
I()mx	I()mx Permissible Load Current			mA
C()mx	Permissible Capacitive Load	50		pF
α()e,mx	Maximum Permissible Acceleration α ()e = $d\omega/dt$ for A/B	200		10 ⁶ rade/s ²
PH	Phase A/B	90	±40	0

AB signals for different mechanical and electrical directions of movement



Startup behavior AB



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